Data Flow Design for the Backpropagation Algorithm

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Abstract

We report a data flow [1] design for the multilayer network. Both back-propagation(BP) [2] learning and feedforward computing are constructed with a single basic module where each neuron is regarded as a module. This design can be extended to various networks [3][4].

1 Introduction

Many data flow designs for neural networks have been developed for various purposes with varied successes, such as that for the committee machine [5]. We report our design in this paper and omit a full review of them. For distributed computation [6], the network training procedure have to be deconstructed such that each neuron can be trained separately. A back-propagation algorithm trains network layer by layer doing forward and backward computations. According to the algorithm the updation formulas [3] are

forward computation

$$y_j^1 = \sigma \left(\sum_{i=0}^{m_0} w_{ji}^1 x_i + b_j^1 \right)$$

for the *j'th* neuron of the first layer, (1)
$$y_j^l = \sigma \left(\sum_{i=0}^{m_{l-1}} w_{ji}^l y_i^{l-1} + b_j^l \right), \{l = 2...L\}$$

$$\int_{a}^{b} \sigma\left(\sum_{i=0}^{b} w_{ji}^{l} y_{i}^{l-1} + b_{j}^{l}\right), \{l = 2...L\}$$

for the j'th neuron of the l'th layer; (2)

backward coputation

$$\begin{split} \delta^L_j &= (d^L_j - y^L_j)(y^L_j)' \\ \text{for the } j'th \text{ neuron of the output layer, } (3) \end{split}$$

$$\delta_j^l = (y_j^l)' \sum_i^{m_{l+1}} \delta_i^{l+1} w_{ij}^{l+1}, \{l = 1...L\}$$

for the j'th neuron of the l'th hidden layer, (4)

 $w_{ji}^{l} = w_{ji}^{l} + \Delta w_{ji}^{l} = w_{ji}^{l} + \eta \delta_{j}^{l} y_{i}^{l-1}$ updation equation. (5)

In the above equations w denotes the weight between two neurons. d is the desired response. x is the input. y denotes the neuron's output. σ is the active function. η is a tunable learning rate. l denotes the number of layer, where 1 denotes the first hidden layer and L is the output layer. i or j denote the number of neuron in each layer. So, y_j^l is the output of the j'thneuron in the $l^\prime th$ hidden layer, w_{ji}^l is the weight between the j'th neuron in the l'th layer and the *i'th* neuron in the (l-1)'th layer. b_i^l is the j'th neuron's bias. d_j^l is the desired response of the j'th neuron in the l'th layer. δ_{j}^{l} is the j'th neuron's delta value for weight correction. m_0 is the number of neurons in input layer, m_{l-1} is the number of neurons in the (l-1)' th layer. All neurons use these equations to improve their weights. Each neuron use the outputs of all neurons in the next precedent layer as inputs. We will isolate each neuron with all its weights, inputs, desired response, and output. This allows us to implement the BP algorithm on distribute parallel machine. In the next section, we present the basic module for a single neuron. Then we show a data flow [1] structure for multilayer networks constructed with such basic module.

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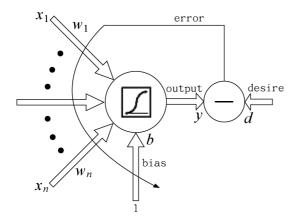


Figure 1: Single neuron diagram.

2 The Basic Module

Fig. 1 shows the diagram of a single neuron. The forward equation is:

$$y^{t} = \sigma \left(\sum_{i=0}^{N} w_{i}^{t} x_{i}^{t} + b \right)$$

$$\tag{6}$$

where t denotes time and the active function is temporarily set to the bipolar sigmoid function $\sigma(u) = \frac{2}{1+e^{-u}} - 1$. According to the delta learning rule [2], the error-correction function is defined by:

$$\Delta w^{t} = \eta \frac{1}{2} \ (d^{t} - y^{t})(1 - (y^{t})^{2})x^{t}.$$
(7)

And the weight is updated according to:

$$w^{t+1} = w^t + \Delta w^t. \tag{8}$$

Combining Eq. 6,7 and setting \mathcal{F} be the updation function for this neuron, we obtain:

$$(\Delta w^t, y^t) = \mathcal{F}(w^t, x^t, d^t), \tag{9}$$

where \mathcal{F} uses w(weight), x(input), and d(desire response) as its inputs and Δw^t and y^t as it's outputs. The data structure for this neuron is plotted in Fig. 2. This structure is well known among the engineering society.

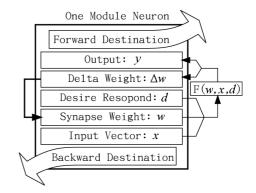


Figure 2: One module neuron data structure.

3 The Modular Design for

Various Neural Network

We can use this basic module to construct various kinds of neural networks, such as multilayer network [3], multilayer network with jump connection [3], recurrent network [8][9] and selforganized map [4]. In this section we show how to construct a multilayer network and recurrent network.

3.1 Multilayer Network

A multilayer network can be transformed into its modular form in O(n) time, where n is the number of total neurons. With a pointer supporting language, such as C [7], we can allocate a memory space for each neuron and maintain a pointer pointing to it. The algorithm is:

Table 1. Modular Transform Algorithm

It is a little cost of memory doing this modular transform. Usually the BP algorithm is implemented using a matrix (or an array) to store weights, input vector and output vector. Each entry in the matrix corresponds to a neuron's relative position in the network. Instead of this matrix, we maintain a pointer for each neuron which contains the synapses to all linked neurons. The key part of the module is that the desired response for each neuron must be given in advance, not only for the neuron in the output layer. Therefore, the BP algorithm (equation 1 to 5) must be reformed in such a way that every hidden neuron can be treated like an independent neuron as long as we can calculate its desired response. For this response, observe the two equations;

$$\delta_j^L = \frac{1}{2} \left(d_j^L - y_j^L \right) \left(1 - (y_j^L)^2 \right), \qquad (10)$$

$$\delta_j^l = \frac{1}{2} \left(1 - (y_j^l)^2 \right) \sum_{i=1}^{m_{l+1}} \delta_i^{l+1} w_{ij}^{l+1} \quad (11)$$

Equation 10 and 11 are the backward delta equation in the BP algorithm using the bipolar sigmoid function. Symbols are defined same as preceding section. Equation 10 is for the the neurons in the output layer and equation 11 is for hidden layer. Simplify these two equations, where we regard y_j^L and y_j^l as the same role, we obtain the desired response for each neuron in the hidden layer,

$$d_j^l = y_j^l + \sum_{i=1}^{m_{l+1}} \delta_i^{l+1} w_{ij}^{l+1}.$$
 (12)

According to $\Delta w_{ij}^L = \eta \delta_i^L y_i^L$, the delta for the neuron i in the output layer is:

$$\delta_i^L = \frac{\Delta w_{ij}^L}{\eta y_i^L} \tag{13}$$

Substitute equation 13 into 12 the desired response for the j'th hidden neuron is:

$$d_j^l = y_j^l + \sum_{i=1}^{m_{l+1}} \frac{\Delta w_{ij}^{l+1}}{\eta y_i^{l+1}} w_{ij}^{l+1}, \{l+1 = 2....L\}.$$
(14)

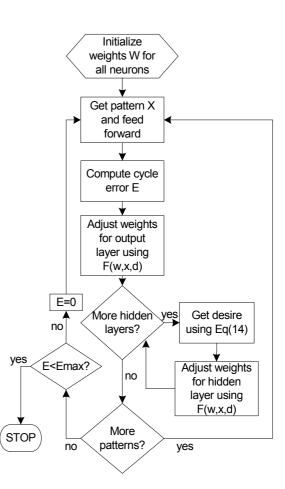


Figure 3: Modified BP training flow of modular design network.

With equation 14 we obtain all neurons' desired responses no matter what layer it belonging to. Therefore each neuron can be treated separately. To our knowledge this equation has not been discussed before.

In figure 3, we illustrate a flow chart of the modular design for the BP algorithm. The main difference between the formal BP algorithm [2] is that we calculate each neuron's desired response before adjust its weights. Figure 5 shows an example of a 1-3-2 modular design network.

Similar to the multilayer feed forward network, a multilayer network can have jump connection (see figure 4) from lower level to higher level. It's forward and backward equations are similar to the BP equations 1 to 5. Its modular

end.

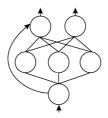


Figure 4: An example of a multilayer network with jump connection.

design is similar to that for the multilayer feed forward network.

3.2 Recurrent Network

A recurrent network [8][9] uses its outputs as its inputs. We show the modular design for training a recurrent network in Figure 6. The training procedure starts by feeding an input vector xinto the modular network. The desired response is set to a target sequence. The output of the network is feedback to itself as the next input in each iteration. The procedure will be continued until the error reduced to a satisfiable range. We treat recurrent network as a normal feed forward network, where we connect it's output destination back to itself.

The modular design is particularly useful for the data flow machine. It is believed to achieve high degree of parallel computation. The main achievement is that we can decompose neural networks into small modules which enable us to feed each module into multi-speed processors that can conform the spirit of data flow machine. The self-organization neural network can cope with the data flow machine structure with less modifications and we omit its discussion.

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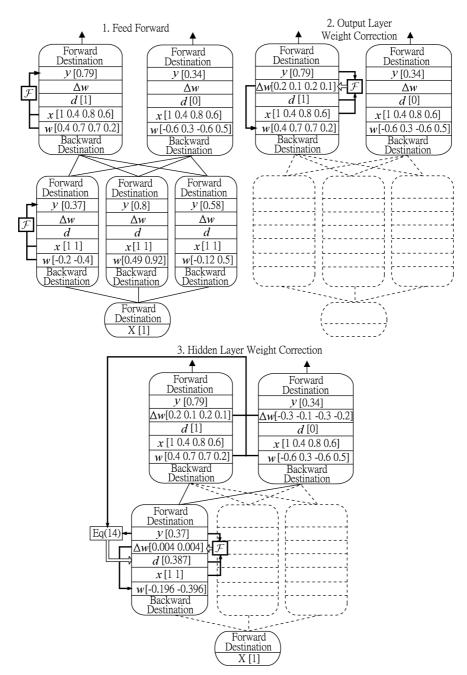


Figure 5: Training procedure of modular design network. The values in step 1 are randomly generated. Succeeding steps change its values according to initial step.

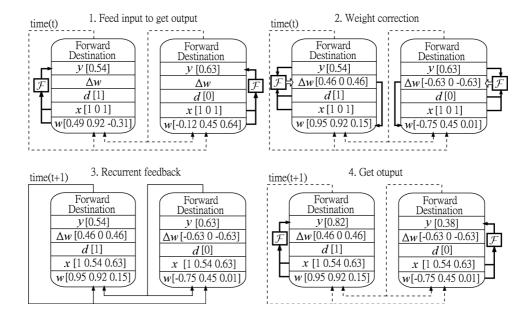


Figure 6: The training procedure of recurrent network using modular design. The values in step 1 are randomly generated. Succeeding steps change its values according to initial step.