Validating SAT Solvers Using an Independent Resolution-Based Checker: Practical Implementations and Other Applications

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ABSTRACT
As the use of SAT solvers as core engines in EDA applications grows, it becomes increasingly important to validate their correctness. In this paper, we describe the implementation of an independent resolution-based checking procedure that can check the validity of unsatisfiable claims produced by the SAT solver zchaff. We examine the practical implementation issues of such a checker and describe two implementations with different pros and cons. Experimental results show low overhead for the checking process. Our checker can work with many other modern SAT solvers with minor modifications, and it can provide information for debugging when checking fails. Finally, we describe additional results that can be obtained by the validation process and briefly discuss their applications.

1. Introduction and Previous Work
Given a propositional logic formula, the question whether it is satisfiable (i.e. exists a variable assignment that makes the formula evaluates to true) is called the Boolean Satisfiability problem, or SAT. SAT is a well-known NP-Complete problem. Many practical EDA problems such as test pattern generation, combinational equivalence checking, microprocessor verification [1], bounded model checking [2] and FPGA routing [3] can be formulated as SAT instances and solved by SAT solvers.

Recent advances in SAT solving algorithms (learning and non-chronological backtracking e.g. [4], random restart [5]) and efficient implementation techniques (e.g. fast implication engine [6]) have dramatically improved the efficiency and capacity of state-of-the-art SAT solving algorithms. SAT solvers have quickly become a viable deduction engine for industrial strength applications (e.g. [7]). As these applications are often mission critical, it is very important to ensure that the results provided by their SAT engines are correct.

Typically, a SAT solver takes a formula as input and produces an output that claims the formula to be either satisfiable or unsatisfiable. A formula is satisfiable if there exists a variable assignment that makes the formula evaluate to true; and is unsatisfiable if no such assignment exists. When the solver claims satisfiability it is usually possible for the solver to provide the satisfying solution with very little overhead. An independent program can take this and verify that it indeed satisfies the formula. The NP-Completeness of SAT guarantees that such a check takes polynomial time in the size of the SAT instance – in fact linear time for Conjunctive Normal Form (CNF) representations used in practice. On the other hand, when a SAT solver claims unsatisfiability, it is usually not trivial for an independent checker to verify the correctness of that claim.

It can be shown that to prove a formula in CNF to be unsatisfiable, we only need to show that an empty clause can be generated from a sequence of resolutions among the original clauses. The classic Davis-Putnam (DP) [8] algorithm is based on this. However, this algorithm is hard to use in practice due to prohibitive space requirements, and over the years has given way to search algorithms based on the Davis-Logemann-Loveland (DLL) algorithm [9]. In this paper, we describe how a checker for a DLL based solver can use the resolution based proof, even though the solver itself is not resolution based in the DP sense. During the solution process, the SAT solver produces a trace. The checker can take the trace together with the original CNF formula as inputs and verify that there indeed exists a resolution sequence to generate an empty clause from the original clauses. If the solver claims that the instance is unsatisfiable but the checker cannot construct an empty clause, then a bug exists in the solver.

The ability to produce checkable proofs in automated reasoning tools is not new. Some theorem provers (e.g. [10]) and model checkers (e.g. [11]) have this ability. Verifying SAT solvers has also been addressed before. In [12], the author mentions that one of the requirements for Stålmarck’s SAT solver is the ability to provide a trace that can be independently checked. However, no details are provided for the checking procedure. That SAT solver is based on proprietary algorithms, so the checking procedure may not be applicable to contemporary DLL based solvers. In [13], the authors provide a SAT procedure that can produce easily checkable proofs. However, it is not obvious that the method employed can be easily adapted to current state-of-the-art solvers. Moreover, in that work an independent checker is not addressed, thus no evaluation of the actual feasibility of checking is provided. To the best of our knowledge, the checker described in this paper is the first for this purpose and that can easily check the correctness of many existing state-of-the-art SAT solvers with little modification.

The checker imposes some requirements on the solver for the generated trace to be sufficient. In particular, we require that the solver be DLL based, and use assertion for backtracking. These requirements will be discussed in later sections. Many widely used state-of-the-art SAT solvers such as GRASP [4], Chaff [6], Berkmin [14] satisfy the requirements and can be easily modified to produce the trace to be checked. In this paper, we focus our discussion and experimentation on the SAT solver zchaff, which is an implementation of the Chaff [6] algorithm, but the same discussion is applicable to other similar SAT solvers.
2. The Boolean SAT solver Chaff

In this section, we describe the algorithms used by Chaff and prove its correctness. This forms the foundation of our checking procedure described in the next sections.

2.1 The Algorithm

Like most SAT solvers, Chaff requires that the input formula be in Conjunctive Normal Form (CNF). To make a CNF formula satisfiable, each clause should be satisfied. If a clause contains only one free (i.e., not assigned a value) literal and all the other literals are assigned the value false, then the free literal must be assigned the value true. Such clauses are called unit clauses and the free literal is called the unit literal. Since a unit literal is forced to be true, we say that it is implied. The unit clause is called the antecedent of the variable corresponding to the implied literal. The process of iteratively assigning all unit literals with the value true till no unit clause left is called Boolean Constraint Propagation (BCP).

The top-level algorithm of Chaff is shown in Figure 1. It begins with all variables being unassigned. Function preprocess() makes some deductions before any decision is made. The preprocessor will perform BCP, and if during BCP a clause has all its literals assigned the value false, we say that a conflict occurred and this clause is said to be conflicting. In that case, the preprocessor returns with value CONFLICT. Otherwise, the main loop begins. The solver chooses a free variable to branch on by assigning it a value. This is called a decision and is performed by decide_next_branch() in Figure 1. In Chaff (as well as most solvers based on DLL), each decision has a decision level associated with it, with the first decision at level 1. The variables that are assigned during preprocessing (before any decision) will have decision level 0.

After a decision variable is assigned a value, some clauses may become unit and function deduce() performs BCP. Variables that are assigned after a branch during BCP assume the same decision level as the decision variable. An important invariant is that a non-free, non-decision variable will always have an antecedent, and its decision level will always equal the highest decision level of the other variables in its antecedent clause. If after BCP no conflict occurs, then the solver will choose another free variable to branch on and increment the decision level. Function decide_next_branch() will return true if it is possible to find a variable to branch on. Otherwise, all variables must have been assigned a value. As no conflicting clause exists, the formula must be satisfiable.

If there exists a conflicting clause during BCP, the function analyze_conflict() is called to analyze the reason for the conflict, perform learning, and find out the decision level to backtrack to. If conflict analysis reports that the solver needs to backtrack to decision level -1 to resolve the conflict, it implies that the formula has conflicts even without any branching, so the solver will claim the problem to be unsatisfiable.

Conflict analysis and determining the backtracking level is crucial to the completeness of the solver, so we will discuss it here in a little more detail. The conflict analysis is based on an operation called resolution. Two clauses can be resolved to generate a resolvent clause as long as there is one and only one variable that appears in both clauses in different phases. The resolvent clause takes the disjunction of the remaining literals in both clauses. An example of resolution is:

\[(x + y) (y' + z) \rightarrow (x + z)\]

with the third clause being the resolvent of the first two. The resolvent is redundant with respect to the original clauses. Therefore, we can generate clauses from original clause database by resolution and add them back to the original clause database without changing the satisfiability of the formula.

The pseudo-code for function analyze_conflict() is given in Figure 2. At the beginning, the function checks if the current decision level is already 0. In that case, the function will return -1, declaring that there is no way to resolve the conflict and the formula is unsatisfiable. Otherwise, iteratively, the procedure resolves the conflicting clause with the antecedent clause of a variable in it. Function choose_literal() will choose a literal in reverse chronological order, meaning that it will choose the literal in the clause that is assigned last. Function resolve(cl1,cl2,var) will return a clause that has all the literals appearing in cl1 and cl2 except for the literals corresponding to var. Notice that the conflicting clause has all literals evaluating to false, and the antecedent clause has all but one literal evaluating to false and the remaining one literal evaluates to true (since it is a unit clause). Therefore, one and only one variable appears in both clauses with different phases and the two clauses can be resolved. The resolvent clause is still a conflicting clause because all its literals evaluate to false, and the resolution process can continue iteratively.

The iterative resolution will stop if the resulting clause meets a stop criterion. The stop criterion is that the resulting clause be an asserting clause. An asserting clause is a clause with all false literals, among them only one literal is at the current decision level and all the others are assigned at decision levels less than current. After backtrack to a level less than the current one, the clause will become a unit clause and this literal will be forced to assume the opposite value, thus bringing the search to a new space. This flipped variable will assume the highest decision level of the rest of the literals in the asserting clause. We call this decision level the asserting level. Function backtrack()
will undo all the variable assignments between the current
decision level and the asserting level and reduce the current
decision level to the asserting level. We call such a backtracking
scheme *assertion based backtracking*. As we will see in the
following sections, this backtracking scheme is key to the
checker we describe in this paper.

The resulting clauses from the resolution can be optionally
added to the clause database. This mechanism is called *learning*.
Learning is not required for the correctness or completeness of
the algorithm. Experiments show that learning can often help
prune future search space and reduce solution time, therefore it
is always employed in modern SAT solvers (e.g. [4] [6] [14]).
Learned clauses can also be deleted in the future if necessary
(e.g. [14]). Regardless of whether the solver employs learning or
not, the clauses that are antecedents of currently assigned
variables should always be kept by the solver because they may
be used in the future resolution process.

### 2.2 The Correctness of the Chaff Algorithm

The algorithm is correct if given enough run time the solver
can always determine the correct satisfiability of the input
formula. Our proof uses the following Lemma.

**Lemma.** A Boolean propositional formula in CNF is
unsatisfiable if it is possible to generate an empty clause by
resolution from the original clauses.

**Proof.** Clauses generated from resolution are redundant and can be added back to
the original clause database. If a CNF formula has an empty clause in it, then it is unsatisfiable. ■

In addition we use the following three propositions.

**Proposition 1.** Given enough run time, the algorithm described in Section 2.1 will always terminate.

**Proof.** We use *k(l)* to denote the number of variables assigned at
decision level *l* in a certain state during the search. Suppose the
input formula contains *n* variables. Because at least one variable
is assigned at each decision level except decision level 0, \( \forall l, 0 \leq l \leq n, k(l) \leq n; \) and \( \sum_{l} k(l) \leq n \). Consider function:

\[
f = \sum_{l} k(l) (n + 1)^{-l}
\]

The function is constructed such that for two sets of different
variable assignment states \( \alpha \) and \( \beta \) of the same propositional
formula, the value \( f_{\alpha} > f_{\beta} \) if and only if

\[
\exists l, 0 \leq l < n, k_{\alpha}(l) > k_{\beta}(l); \forall l \text{ such that } 0 \leq l < d, k_{\alpha}(l) = k_{\beta}(l)
\]

Essentially this function biases the sum towards the number of
variables assigned at lower decision levels. The value of *f*
monotonically increases as the search progresses. This is
obvious when no conflict exists, because the solver will keep
assigning variables at the highest decision level. When a conflict
occurs, assertion based backtracking moves an assigned variable
at the current decision level to the asserting level, which is
smaller than the current decision level. Therefore, the value of *f*
still increases compared with its value before conflict analysis.

Because function *f* can only take a finite number of different
values for a given SAT instance, the algorithm is guaranteed to
terminate after sufficient run time. ■

Notice that in our proof for termination, we do not require
the solver to keep all learned clauses; the value of function *f*
will monotonically increase regardless whether any learned clauses
are added or deleted. Therefore, contrary to common belief,
deleting learned clauses cannot cause the solving process to loop
indefinitely. On the other hand, with restarts [5] it is actually
possible for the solver to loop indefinitely because after each
restart, all the variables are unassigned, and the value of *f*
decreases. Therefore, it is important for the solver to increase
the restart period as the solving progresses to make sure that the
algorithm will terminate.

**Proposition 2.** If the algorithm described in Section 2.1 returns
SATISFIABLE, then the formula is satisfiable.

**Proof.** If all variables are assigned a value and no conflict
exists, the formula is obviously satisfied. ■

**Proposition 3.** If the algorithm described in Section 2.1 returns
UNSATISFIABLE, then the formula is unsatisfiable.

**Proof.** We prove this proposition using the Lemma. We will
show how to construct an empty clause from the original clauses
by resolution.

When the solver returns UNSATISFIABLE, the last
analyze_conflict() function must have encountered a
conflicting clause while the current decision level is 0. Starting
from that clause, we iteratively do a resolution similar to the
while loop shown in Figure 2. The only difference is that the
stop criterion is changed so that the iteration will stop only if the
resulting clause is an empty clause. Because there is no decision
variable at decision level 0, every assigned variable must have
an antecedent. Since function choose_literal() chooses
literals in reverse chronological order, no literal can be chosen
twice in the iteration and the process will stop after at most *n*
calls to resolve(). At that moment, an empty clause is
generated. All the clauses involved in the resolution process are
either original clauses or learned clauses that are generated by
resolution from the original clauses; therefore by the Lemma,
the original formula is indeed unsatisfiable. ■

### 3. A Resolution Based Checker

In Section 2, we prove that the algorithm of Chaff is sound
and complete. However, a SAT solver is a complex piece of
code, and consequently the implementation may have bugs in it.
In fact, during the recent SAT 2002 solver competition [15],
quite a few submitted SAT solvers were found to be buggy.
Thus, a rigorous checker is needed to validate the solvers. In this
section, we describe the checker that is derived from the results
of last section. The checker can prove a formula to be
unsatisfiable when a SAT solver claims so.

#### 3.1 Producing the Trace for the Checker

For the checker to be able to verify that a SAT instance is
indeed unsatisfiable, it only needs to verify that there exists a
sequence of resolution among the original clauses that can
generate an empty clause. To achieve this, we need to modify
the SAT solver to generate a trace that can be used for the
resolution checking process of the checker. We assign
each clause encountered in the solution process a unique ID. The
original clauses have IDs that are agreed to by both the solver
and the checker (e.g. the order of appearance in the formula).

The trace is produced by modifying the solver in the
following ways:

1. Each time a learned clause is generated, the clause’s ID is
recorded, together with the clauses that are involved in
generating this clause. In Figure 2, these include the clause
returned by find_conflicting_clause(), as well as
all the clauses that correspond to parameter ante in the
calls to resolve(). We will call these clauses the
resolve sources of the generated clause.

2. When analyze_conflict() is called and the current
decision level is 0, the solver will record the IDs of the
clauses that are conflicting at that time before returning value -1. We call these clauses the final conflicting clauses. In the proof process, we only need one of these clauses to construct a proof, so it is sufficient to record only one ID.

3. When analyze_conflict() returns with value -1, before returning with value UNSATISFIABLE, the solver will record all the variables that are assigned at decision level 0 together with their values and the IDs of their antecedent clauses in the trace.

These modifications total less than twenty lines of C++ code, and should be very easy for other SAT solvers that are based on the same assertion based backtracking technique such as GRASP [4] and BerkMin [14].

The checker will take the trace together with the original formula and try to generate an empty clause in a manner similar to the description of the proof for Proposition 3 in Section 2.2. However, initially the checker does not have the actual literals of the learned clauses. Instead, from the trace file the checker only knows the resolve sources’ IDs that are used to generate each of these learned clauses. Therefore, to do the resolution, the checker needs to use resolution to produce the needed learned clauses first. Essentially the checker creates and traverses the resolution graph, which is a directed acyclic-graph that describes the sequence of resolutions starting from the original clauses at the leaves and ending with the empty clause at the root. There are two different approaches for this traversal: depth first (Section 3.2) and breadth first (Section 3.3).

3.2 The Depth First Approach

The depth first approach for building the learned clauses begins from any one of the final conflicting clauses. It builds the needed clauses by resolution recursively as described in Figure 3 in function check_depth_first().

```c
check_depth_first() {
    id = get_one_final_conf_clause_id();
    while(id != NULL) {
        cl = resolve(cl,cl1);
        ante_id = antecedent_id(var);
        ante_cl = resolve(cl,ante_cl);
        cl = resolve(cl,ante_cl);
    }
    if (error_exist())
        printf("Check Failed");
    else
        printf("Check Succeeded");
}
```

**Fig. 3.** A depth-first approach for the checker

If a clause is needed, it will be built on the fly using function recursive_build(). The checking for the checker is built in at each of the functions it employs. For example, when resolve(cl, cl1) is called, the function should check whether there is one and only one variable appearing in both clauses with different phases; when a variable’s antecedent clause is needed, the function should check whether the clause is really the antecedent of the variable (i.e. whether it is a unit clause and whether the unit literal corresponds to the variable). If such checks fail, the solver (or its trace generation) is buggy. The checker can also provide as much information as possible about the failure to help debug the solver.

The advantage of the depth-first approach is that the checker builds only the clauses that are involved in the empty clause generation process. Also, as a by-product, the checker can tell what clauses are needed for this proof of unsatisfiability. This gives us a minimal set of clauses in the original propositional formula that are needed for this particular proof. There are several applications that need small unsatisfiable subsets of an unsatisfiable instance (e.g. [16]), where this can be used.

The disadvantage of the depth-first approach is that in order to make the recursive function efficient, the checker needs to read in the entire trace file into main memory. This can be quite large and possibly not fit in memory. Therefore, for some long proofs, the checker may not be able to complete the checking.

3.3 The Breadth-First Approach

A breadth-first approach starting from the leaves can avoid keeping the entire trace file in memory as the depth-first solver does. The checker traverses the learned clauses in the order that they are generated in the solving process, which is the same order as they appear in the trace file. When generating the final empty clause, all its resolution sources should have all been constructed and available in memory.

However, we will still have the memory blowup problem because the checker will have all the learned clauses in memory before the final empty clause construction begins. It is well known that storing all the learned clauses in memory is often not feasible. Note however that because of the breadth first nature, a clause can be deleted once its use as a resolve source is complete. A first pass through the trace can determine the number of times a clause is used as a resolve source. During the resolution process, the checker tracks the number of times the clause has been used as a resolve source and when its use is complete, the clause can be deleted safely from main memory.

In the actual implementation, the clause’s total use count is stored in a temporary file because there is a possibility that even keeping just one counter for each learned clause in main memory is still not feasible. Due to the same reason, we may also need to break the first pass into several passes so that we can count the number of usages of the clauses in one range at a time. The checker is guaranteed to be able to check any proof produced by a SAT solver without danger of running out of memory because during the resolution process, the checker will never keep more clauses in the memory than the SAT solver did when producing the trace. Because the SAT solver did not run out of memory (it finished and claimed the instance to be unsatisfiable), the checker will not run out of memory during the checking process either; assuming we run both programs with the same amount of memory.

4. Experimental Results

In this section we report some experimental results. The experiments are carried out on a PIII 1133Mhz machine with 1G memory. We set the memory limit to 800MB for the checkers. The benchmarks we use are some relatively large unsatisfiable instances that are commonly used for SAT solver
check the proof as well as the peak memory usage. Comparing
and breadth-first approaches, we show the actual run time to
often require long proofs by resolution. For both the depth-first
circuit contains many xor gates. It is well known that xor gates
longmult12, which is derived from a multiplier. The original
built to check the proofs. A notable exception to this is
long time to finish often need a smaller percentage of clauses
check the proof. Moreover, we see that the instances that take a
Learned Clauses" in Table 1. As we can see from Table 2, we
"Built%" shows the ratio of the clauses that are built to the total
solver. For the depth-first approach, the column "Num. Cls
overhead for producing the trace file (1.7% to 12%) is not
as the original zchaff) and turned on. From Table 1 we find that
of the proofs in Table 2. The column "File Size" shows the
benchmarks. We want to point out that the format of the trace
files produced by the SAT solvers are quite large for hard
benchmarks. We want to point out that the format of the trace
file we use is not very space-efficient in order to make the trace
human readable. It is quite easy to modify the format to
emphasize space efficiency and get a 2-3x compaction (e.g. use
binary encoding instead of ASCII). By doing so, we also expect
the efficiency of the checker to improve as profiling shows that
a significant amount of run time for the checker is spent on
parsing and translating the trace files.
In Table 3, we show the number of original clauses and
variables that are involved in the actual proof by the depth-first
approach. The “original” columns show the statistics of the
original benchmark from AI planning community (bw_large).
In contrast, even though the breadth-first approach is slower
compared with the depth-first approach, it is able to finish all
benchmarks with a relatively small memory footprint.
From Table 2 we find that the actual time needed to check a
proof is always significantly smaller compared with the time
needed to perform the actual proof. We also find that the trace
files produced by the SAT solvers are quite large for hard
benchmarks. We want to point out that the format of the trace
file we use is not very space-efficient in order to make the trace
human readable. It is quite easy to modify the format to
emphasize space efficiency and get a 2-3x compaction (e.g. use
binary encoding instead of ASCII). By doing so, we also expect
the efficiency of the checker to improve as profiling shows that
a significant amount of run time for the checker is spent on
parsing and translating the trace files.
In Table 3, we show the number of original clauses and
variables that are involved in the actual proof by the depth-first
approach. The “original” columns show the statistics of the
original benchmark. The variable numbers may be a little
different from the ones reported in Table 1. Table 1 shows the variable numbers declared by the header of the CNF file, but
some of the variables are actually not used in the formula. The
"First iteration" column shows the number of original clauses and
variables needed for the checker to prove the
unsatisfiability. Essentially these numbers show that not all the
clauses in the original CNF formula are needed for it to be
unsatisfiable. We can use these involved clauses as a new SAT
instance. The new instance should still be unsatisfiable. We can
feed it back to the SAT solver and iteratively perform the depth-
first checking again to determine the number of clauses needed

<table>
<thead>
<tr>
<th>Instance Name</th>
<th>Trace Size (KB)</th>
<th>Num. Cls</th>
<th>Built%</th>
<th>Runtime(s)</th>
<th>Peak Mem (KB)</th>
<th>Overhead</th>
</tr>
</thead>
<tbody>
<tr>
<td>2dlx_cc_mc_ex_bp_f</td>
<td>1261</td>
<td>8105</td>
<td>84.83%</td>
<td>0.84</td>
<td>7860</td>
<td>1.30</td>
</tr>
<tr>
<td>bw_large.d</td>
<td>1367</td>
<td>5513</td>
<td>77.21%</td>
<td>1.48</td>
<td>8720</td>
<td>2.44</td>
</tr>
<tr>
<td>c5315</td>
<td>11337</td>
<td>27516</td>
<td>54.71%</td>
<td>2.80</td>
<td>18108</td>
<td>5.19</td>
</tr>
<tr>
<td>too_largefs3w8v262</td>
<td>8868</td>
<td>56193</td>
<td>61.28%</td>
<td>3.79</td>
<td>26752</td>
<td>5.47</td>
</tr>
<tr>
<td>c7552</td>
<td>24355</td>
<td>56803</td>
<td>56.33%</td>
<td>6.31</td>
<td>41410</td>
<td>11.28</td>
</tr>
<tr>
<td>5pipe_5_ooo</td>
<td>17466</td>
<td>24910</td>
<td>31.23%</td>
<td>6.60</td>
<td>50044</td>
<td>13.29</td>
</tr>
<tr>
<td>barrel9</td>
<td>19569</td>
<td>34624</td>
<td>26.00%</td>
<td>4.85</td>
<td>31424</td>
<td>10.46</td>
</tr>
<tr>
<td>longmult12</td>
<td>102399</td>
<td>118615</td>
<td>10.90%</td>
<td>25.87</td>
<td>154289</td>
<td>41.22</td>
</tr>
<tr>
<td>9wilw_bp_mc</td>
<td>11363</td>
<td>5725</td>
<td>71.27%</td>
<td>1.48</td>
<td>8720</td>
<td>2.44</td>
</tr>
<tr>
<td>6pipe_6_ooo</td>
<td>15809</td>
<td>394739</td>
<td>132733</td>
<td>4106.7</td>
<td>40248</td>
<td>4.26</td>
</tr>
<tr>
<td>6pipe</td>
<td>23910</td>
<td>751118</td>
<td>261392</td>
<td>13672.8</td>
<td>13902.4</td>
<td>1.68</td>
</tr>
</tbody>
</table>

Table 1: Statistics of zchaff with trace generation turned on and off

<table>
<thead>
<tr>
<th>Instance Name</th>
<th>Trace Size (KB)</th>
<th>Num. Cls</th>
<th>Built%</th>
<th>Runtime(s)</th>
<th>Peak Mem (KB)</th>
<th>Overhead</th>
</tr>
</thead>
<tbody>
<tr>
<td>7pipe</td>
<td>751118</td>
<td>261392</td>
<td>13672.8</td>
<td>13902.4</td>
<td>1.68</td>
<td></td>
</tr>
</tbody>
</table>

Table 2: Statistics for two different checking strategies (* Indicates Memory Out)
for an unsatisfiability proof for it. After several iterations, the number may reach a fixed point, so that all the clauses are needed for the proof. We measured up to 30 iterations of such a process, and the data is reported under the column “30 iterations (or reach fixed point)”. If the “iteration” number is smaller than 30, it means that after a certain number of iterations, all clauses are needed in the proof.

The process described above can be used to determine a small sub-formula (an *unsatisfiable core*) of a CNF formula that is unsatisfiable. This unsatisfiable core is useful in some applications such as debugging software models in the Alloy system [17], FPGA routing [2] and AI planning. For example, in AI planning, a satisfiable solution corresponds to a feasible system [17], FPGA routing [2] and AI planning. Notice from Table 3, the instances from both AI planning and FPGA routing (too_largefs3w8v262) have small unsatisfiable cores compared with the original formulas.

<table>
<thead>
<tr>
<th>Benchmark Instance</th>
<th>Original</th>
<th>First Iteration</th>
<th>30 Iterations (or reach fixed point)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Num Cls</td>
<td>Num Vars</td>
<td>Num Cls</td>
</tr>
<tr>
<td>2dx_cc_mc_ex_bp_df</td>
<td>41704</td>
<td>4524</td>
<td>11169</td>
</tr>
<tr>
<td>bw_large.d</td>
<td>122412</td>
<td>5888</td>
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Table 3. Number of Clauses and Variables involved in the proof.

5. Conclusions and Future Work

In this paper, we describe a checker for Boolean Satisfiability solvers. When a SAT solver proves that a SAT instance is unsatisfiable, the checker can take a trace produced by the SAT solver and check whether the proof is actually valid. The checker uses resolution to generate an empty clause from the original clause database with the information provided by the trace. We discuss two approaches for implementing the checker. Experiments show that both approaches have their relative merits. In all cases, the time needed to check a proof is much less than the SAT solving time.

It is desirable to have a checker that has the advantage of both the depth-first and breadth-first approaches without suffering from their respective shortcomings. Potentially a depth-first algorithm for the graph on disk [18] can provide this.

6. References