Formal Verification in practice: system software perspective

時間：2017/4/19(三) 9:00-11:00
地點：台灣大學德田館R101教室
講者：黃敬群 (Jim Huang)

== bio ==
黃敬群 (Jim Huang)，網路慣用暱稱 “jserv” 參與過若干世界級的開放原始碼專案，如 Android Open Source Project, GCC, LXDE。過去耕耘於消費性電子產品、IC 設計與工業自動化等領域。近來和一群「火箭大叔」合作，在原 ARRC (前瞻火箭研究中心) 的基礎下，強化火箭控制系統的軟體架構。

== abstract ==
Recent system software development is executed with various verification techniques. seL4 is one of the typical models, where large-scale formal software verification projects runs successfully. In this talk, we would report on the development process about formulating a detailed descriptive model of its middle-out development process, and analyze the evolution and dependencies of code and proofs. Formal verifications in industry are becoming common. Microsoft uses formal tools such as Boogie and SLAM to verify programs and drivers, so does Airbus and Toyota.